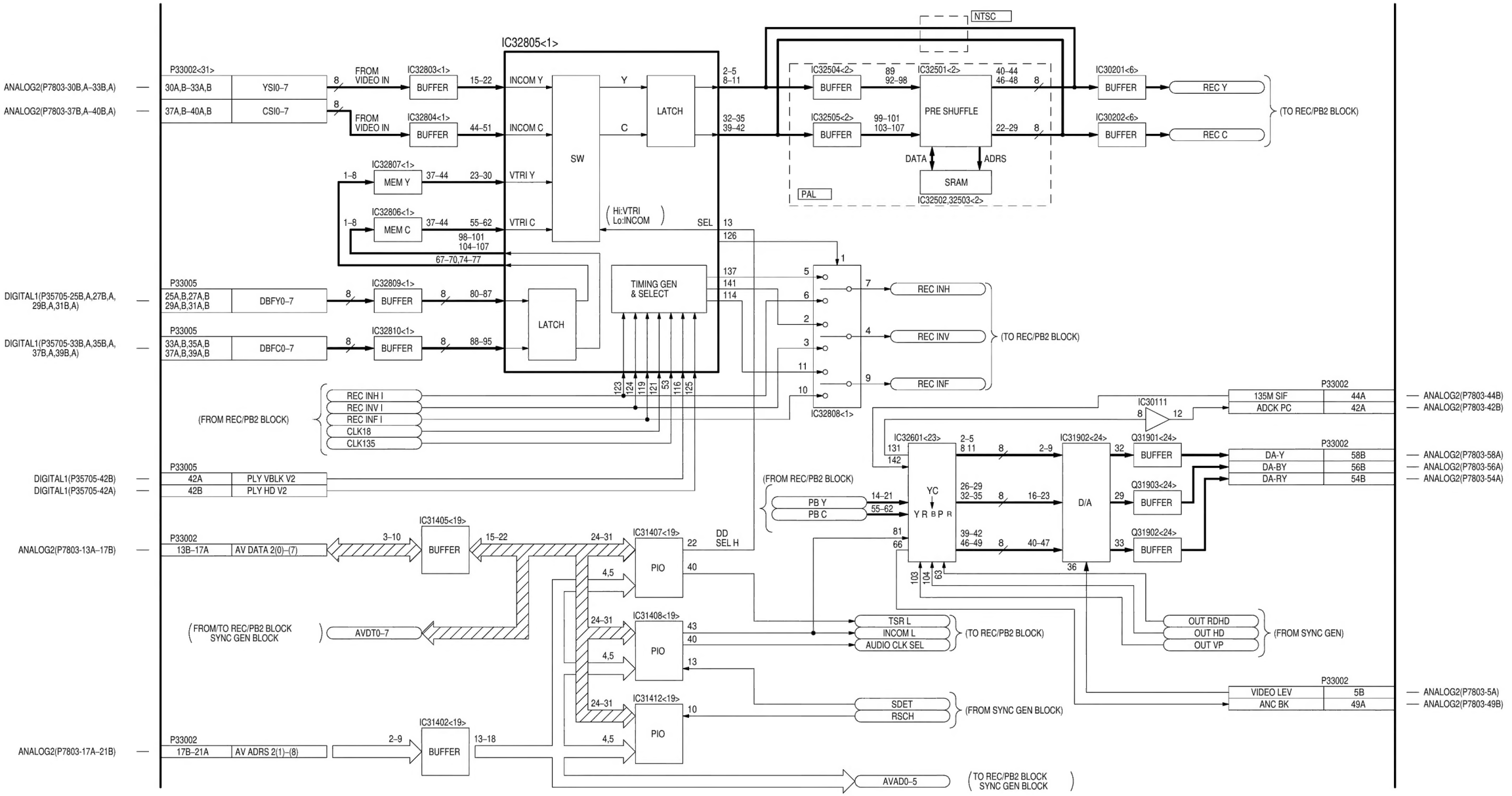
IC32805<1>
NTSC
PAL
ANALOG2(P7803-30B,A–33B,A)
ANALOG2(P7803-37B,A–40B,A)
P33002<31>
30A,B–33A,B
37A,B–40A,B
YSI0–7
CSI0–7
FROM VIDEO IN
IC32803<1>
IC32804<1>
BUFFER
15–22
44–51
INCOM Y
INCOM C
SW
Y
C
LATCH
2–5
8–11
32–35
39–42
IC32504<2>
IC32505<2>
89
92–98
99–101
103–107
IC32501<2>
PRE SHUFFLE
40–44
46–48
22–29
DATA
ADRS
SRAM
IC32502,32503<2>
IC30201<6>
IC30202<6>
REC Y
REC C
(TO REC/PB2 BLOCK)
IC32807<1>
IC32806<1>
MEM Y
MEM C
1–8
37–44
23–30
55–62
VTRI Y
VTRI C
Hi:VTRI
Lo:INCOM
SEL
13
126
98–101
104–107
67–70,74–77
DIGITAL1(P35705-25B,A,27B,A,
29B,A,31B,A)
DIGITAL1(P35705-33B,A,35B,A,
37B,A,39B,A)
P33005
25A,B,27A,B
29A,B,31A,B
33A,B,35A,B
37A,B,39A,B
DBFY0–7
DBFC0–7
IC32809<1>
IC32810<1>
80–87
88–95
LATCH
TIMING GEN
& SELECT
137
141
114
IC32808<1>
REC INH
REC INV
REC INF
(TO REC/PB2 BLOCK)
(FROM REC/PB2 BLOCK)
REC INH I
REC INV I
REC INF I
CLK18
CLK135
123
124
119
121
53
116
125
P33002
135M SIF
ADCK PC
44A
42A
IC30111
ANALOG2(P7803-44B)
ANALOG2(P7803-42B)
DIGITAL1(P35705-42B)
DIGITAL1(P35705-42A)
42A
42B
PLY VBLK V2
PLY HD V2
IC32601<23>
131
142
YC
Y R B P R
(FROM REC/PB2 BLOCK)
PB Y
PB C
14–21
55–62
IC31902<24>
D/A
Q31901<24>
Q31903<24>
Q31902<24>
BUFFER
26–29
32–35
16–23
39–42
46–49
40–47
81
66
103
104
63
36
DA-Y
DA-BY
DA-RY
58B
56B
54B
ANALOG2(P7803-58A)
ANALOG2(P7803-56A)
ANALOG2(P7803-54A)
IC31405<19>
IC31407<19>
IC31408<19>
IC31412<19>
IC31402<19>
PIO
ANALOG2(P7803-13A–17B)
P33002
13B–17A
AV DATA 2(0)–(7)
3–10
24–31
4,5
22
40
43
13
10
DD
SEL H
(FROM/TO REC/PB2 BLOCK
SYNC GEN BLOCK)
AVDT0–7
TSR L
INCOM L
AUDIO CLK SEL
SDET
RSCH
(FROM SYNC GEN BLOCK)
OUT RDHD
OUT HD
OUT VP
(FROM SYNC GEN)
VIDEO LEV
ANC BK
5B
49A
ANALOG2(P7803-5A)
ANALOG2(P7803-49B)
ANALOG2(P7803-17A–21B)
17B–21A
AV ADRS 2(1)–(8)
2–9
13–18
AVAD0–5
(TO REC/PB2 BLOCK
SYNC GEN BLOCK)